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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bedichek, et al.

Serial No. : 09/417,332 Group Art Unit: 2183

Filed : 10/13/1999 Examiner: Ellis, R.

For : METHOD FOR INTEGRATION OF INTERPRETATION AND TRANSLATION IN A MICROPROCESSOR

RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents & Trademarks
Washington, D.C. 20231

Sir:

In response to the Office Action mailed 2/13/04, please consider the following amendments and remarks.